## ENX 16 EASY INT Encoder Ø16 mm,

## 1...1024 CPT/4096 steps, Single Turn

Sterilizable, integrated into motor



Key Data	EASY Incremental Differential	EASY Absolute
Number of channels	3	<u>'</u>
Max. counts per turn	1024	
Steps per turn		4096
Resolution (bit single turn)		12
Encoder length L	mm -1 (integrated into motor)	-1 (integrated into motor)
Ambient temperature	°C -40100	-40100
Weight	g <5	<5

Selection criteria	<b>EASY Incremental Differential</b>	<b>EASY Absolute</b>	
Speed and rotation direction detection			
Speed and position control			
Compact and robust design			
High resolution		•	
Cost effective			

■ suitable ▲ suitable to a limited extent ● not suitable

Specifications	EASY Incremental Differential <sup>3</sup>	EASY Absolute
Supply voltage Vcc	V 5 ± 0.5	5 ± 0.5
Typical current draw	mA 22	22
Max. operating frequency	kHz 4000	
Max. Speed	rpm 200 000	200 000
Connector <sup>2</sup>	10-pin 2.54 mm multipoint connector (IEC/EN 60603-13 / DIN41651) cable AWG 28  Pin 1 Not connected Pin 2 (black) V <sub>cc</sub> Pin 3 (brown) GND  Pin 4 Not connected Pin 5 (red) Channel Ā  Pin 6 (orange) Channel A  Pin 7 (yellow) Channel B  Pin 8 (green) Channel B  Pin 9 (blue) Channel I  Pin 10 (violet) Channel I	without connector  cable AWG 28 green Data black V <sub>cc</sub> brown GND yellow CLK
	Output signal: EIA-Standard RS 422 Output current per channel: ± 20 mA	Output signal: CMOS compatible Output current per channel + 20 mA

Configuration		EASY Incremental Differential	EASY Absolute
Counts per turn <sup>1</sup>		11024	
Signalprotokoll			BiSS-C, SSI
Cable length	mm	200, 500	200, 500
Electric connection			cable length/pin connection/connector

Electric connection				cable length/pin connection/connector
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maxon Modular System maxon EC motor	Page Ster	ilization information		Notes
ECX SPEED 16 M ECX SPEED 16 L	195-198 199-202		+134 ± 4°C 2.3 bar 100% 18 minutes	<ul> <li><sup>1</sup> maxon controllers require a resolution of at least 16 counts per turn and commutation signals.</li> <li><sup>2</sup> H1, index and angle zero are aligned with angle commutation zero (see p. 56).</li> </ul>
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